Scheme of Instruction, Evaluation

and

Syllabi of

B.E. WORKING PROFESSIONALS (ELECTRONICS AND COMMUNICATION ENGINEERING)

With effect from Academic Year 2025-26

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Estd.1917

UNIVERSITY COLLEGE OF ENGINEERING Estd.1929

(Autonomous)

Osmania University

Hyderabad – 500 007, TS, INDIA

SCHEME OF INSTRUCTION AND EVALUATION
B. E (ELECTRONICS AND COMMUNICATION ENGINEERING)
SEMESTER-III

(With effect from AY: 2025-26)

S. No	Code	Course Title	Scheme of Instruction			Contact Hrs/Wk	Scheme of Evaluation			Credits
			L	T	P		Hrs	CIE	SEE	
Theory										
1	PC301EC	Analog Electronics-I	3	-	-	3	3	40	60	3
2	PC302EC	Signal Analysis and Transform Techniques	3	-	-	3	3	40	60	3
3	PC303EC	Switching Theory and Logic Design	3	-	-	3	3	40	60	3
4	PC304EC	Pulse and Digital Circuits	3	-	-	3	3	40	60	3
5	PC305EC	Network Analysis and Synthesis	3	-	-	3	3	40	60	3
Practicals										
6	PC351EC	Pulse and Digital Circuits Lab	-	-	3	3	3	25	50	1.5
7	PC352EC	Network Analysis and Synthesis Lab	-	-	3	3	3	25	50	1.5
Total			**15**	**-**	**6**	**21**	**21**	**250**	**400**	**18**

PC 301 EC	ANALOG ELECTRONICS -I					
Pre-requisites	Electronic devices and Circuits		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	Learn concepts of Small signals amplifiers
2	Study the design concepts Low Frequency transistor amplifiers.
3	Learn concepts of transistor amplifiers at high frequency.
4	Design concepts of multi stage amplifiers.
5	Have a basic knowledge of Operational-Amplifiers.

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Design and Analyze small signal single stage
CO-2	Design and Analyze Multistage RC coupled and Transformer Amplifiers using BJT and FET.
CO-3	Analyze of low frequency, mid frequency and high frequency response of Multistage amplifiers
CO-4	Analyze of high input resistance transistor circuits.
CO-5	have a basic concept of characteristics Operational-Amplifiers;

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	-	-	-	-	-	-	-	-	-	-	-	-
CO2	3	2	1	2	-	-	-	-	-	-	-	-	1	-
CO3	3	2	2	-	-	-	-	-	-	-	-	-	1	-
CO4	2	2	2	-	-	-	-	-	-	-	-	-	1	-
CO5	3	2	2	2	-	-	-	-	-	-	-	-	-	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT–I

Small Signal – Low Frequency Transistor Model: Two Port Devices and Hybrid Model, hParameters and Measurement. Conversion Formulas for the Parameters of the Three Transistor Configurations. Analysis of Transistor Amplifier Circuit using h-parameters. Comparison of CB, CE and CC Amplifier Configurations. Linear Analysis of a Transistor Circuit with Appropriate Model. The FET Small Signal Model, Common Source and Common Drain Amplifier Circuits.

UNIT–II

Transistor at high frequencies: High frequency T-model, Miller theorem, the CB Short circuit current Frequency Response, The Alpha cutoff frequency, the CE Short circuit current, Frequency Response, Hybrid PI model CE short circuit current gain obtained with the Hybrid-Pi model and resistive load. Transistor amplifier response with source resistance, Gain-Band width product.

UNIT–III

Multistage Amplifiers: Classification of amplifiers, Distortion in amplifiers, frequency response of RC-coupled, single stage, Transformer coupled amplifier and their analysis. Step response of amplifier, rise time, tilt, slag, square wave testing, interacting and non interacting stage, effect of emitter by pass capacitor on low frequency response.

UNIT-IV

Low frequency transistor amplifier circuits: cascading transistor amplifiers, n-stage cascaded amplifier, the decibel, high input resistance transistor circuits, cascade transistor reconfiguration.

UNIT – V

Operational Amplifiers: Classification of Integrated Circuits, Operational Amplifier Block Diagram, Ideal and practical characteristics of Op-Amps, Op-Amp features and parameters. OpAmp measurements, input and output offset voltages and currents, Slew rate, CMRR, PSRR, frequency response.

Suggested Reading:

1	Millman J., Halkias C.C. and Satyabrata Jit, *Electronic Devices and Circuits,* 3rd edition, Tata McGraw-Hill, 2011.
2	S Salivahanan, N Kumar, and A Vallavaraj, Electronic Devices and Circuits, 2nd ed., McGraw Hill Education, 2007.
3	Millman J., Halkias C.C. and Parikh C, *Integrated Electronics,* 2nd edition, Tata McGraw-Hill, 2009.
4	JB Gupta, *Electronic Devices and Circuits*, S.K Kataria & sons, 5th Edition, 2012
5	Ramakanth A. Gayakwad, "*Op-amps and Linear Integrated Circuits*", 3rd Edition, Prentice-Hall of India private Limited, New Delhi, 1995.

PC 302 EC	SIGNAL ANALYSIS AND TRANSFORM TECHNIQUES					
Pre-requisites	-		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To familiarize different types of signals and systems typically encountered in Communication engineering.
2	To familiarize with basic operations on signals and mathematical representation of signals
3	To understand the behavior of signal in time and frequency domain
4	To understand convolution, correlation operations on continuous and discrete time signals
5	To analyze the response of systems on application of step, ramp inputs using Laplace & Z-transforms

Course Outcomes:	
On completion of this course, the student will be able to:	
CO-1	Be able to describe signals mathematically and understand how to perform mathematical operations on signals
CO-2	Understand and resolve the signals in frequency domain using Fourier series and Fourier transforms
CO-3	Be able to compute the output of an LTI system given the input and impulse response through convolution sum and convolution integral
CO-4	Understand the sampling theorem and the process of reconstructing a continuous signal from its samples
CO5	Be able to solve a linear constant coefficient difference equation using Z transform techniques

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	3	1	1	-	-	-	-	2	-	-	3	-
CO2	2	2	2	2	1	1	-	-	-	1	-	-	3	-
CO3	2	1	2	2	2	2	-	-	-	2	-	-	3	-
CO4	3	2	2	2	2	1	-	-	-	2	-	-	2	-
CO5	2	3	2	3	2	1	-	-	-	1	-	-	2	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT – I

Introduction to Signals & Systems: Classification of signals, elementary signals, Operations on signals, classification of systems, Exponential and Trigonometric Fourier series, Dirichlet‟s condition

UNIT – II

Fourier Transform: Representation of aperiodic signal, Introduction of Fourier transform, Convergence, properties of Fourier Transform, Fourier transform of periodic signals, Singularity function, Parseval‟s theorem, Energy spectral density.

UNIT – III

Laplace Transform: Review of Laplace transforms, region of convergence and properties, poles and zeros, relation between Laplace and Fourier transforms, properties of Laplace transform, inverse Laplace transform, Solutions to differential equation and system behavior.

UNIT – IV

Sampling: Sampling of continuous time signals, sampling theorem, Aliasing effect, reconstruction of a signal and its samples.

Convolution & Correlation of signals: Convolution integral, Properties of convolution, Graphical method of convolution, Convolution of Discrete time signals, overlap-add and overlap-save method of discrete convolution, Definition of correlation, Auto correlation, Properties of Autocorrelation, Cross correlation of signals..

UNIT – V

Z Transform: Definition of Z-Transform, Properties of Z-Transform, Region of convergence of Z-Transform, Inverse Z Transform using Inspection, Partial fraction expansion, Power series Expansion, Contour integration methods, Parseval‟s relation analysis of discrete time systems using Z-Transform. Realization of discrete time system using Direct form, Cascade parallel forms.

Suggested Reading:

1	Alan V. Oppenheim, Alan. S. Wilsky, S. Hamid Nawab, *Signals and Systems*, 2nd edition, Prentice Hall of India, 2007
2	Lathi B.P., *Signals Systems Communications*", 1st edition, B.S. Publications, 2006
3	Simon Haykin and Van veen, "Signal and system", Willy, second edition

PC 303 EC	SWITCHING THEORY AND LOGIC DESIGN					
Pre-requisites	Engineering Mathematics		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:

The course is taught with the objectives of enabling the student to:	
1	To introduce common forms of number representation in logic circuits.
2	To familiarize with basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
3	To understand the concepts of combinational logic circuits.
4	To understand the concepts of sequential logic circuits.
5	To familiarize with various logic families in Digital ICs.

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Acquire the knowledge on number systems and Boolean Algebra theorems to minimize combinational functions.
CO-2	Design various combinational circuits.
CO-3	Acquire knowledge on usage of Flip-flops in various circuits
CO-4	Design Sequential circuits for the given specifications.
CO-5	Acquire knowledge on Logic families and their interfacing

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	3	1	2	1	-	-	-	-	-	2	1	-
CO2	2	3	3	2	2	1	-	-	-	-	-	1	1	-
CO3	2	3	3	2	2	1	-	-	-	-	-	1	1	-
CO4	3	2	1	1	1	-	-	-	-	-	-	2	1	-
CO5	2	2	3	2	2	-	-	-	-	-	-	2	1	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT–I

Number Systems

Number systems, Conversion and Complements Codes- Weighted and Non-weighted codes and their Properties.

Boolean Algebra

Basic Theorems and Properties, Switching Functions- Canonical and Standard Forms, Function Simplification using Theorems, Digital Logic Gates, Universal Gates, Two level NAND/NOR realization of Boolean Function, Multilevel realization of Boolean Function.

Minimization with Theorems

Karnaugh Map Method - Up to four Variables, Don‟t Care Map Entries, Tabular Method

UNIT–II

Combinational Logic Circuit Design and Applications

Adders, Subtractors, comparators, Multiplexers, De-multiplexers, Encoders, Decoders and Codeconverters, Hazards and Hazards Free Realizations. Practical aspects related to Combinational Logic Design- Fan-in and Fan-out, Propagation Delay.

UNIT–III

Flip-flops:

Architectural difference of Combinational and Sequential circuits, SR Latch, Types of Traditional Clocked Flip Flops: SR, JK, JK Master Slave, D and T Type Flip Flops, Excitation Tables of all Flip Flops, Conversion from one type of Flip-Flop to another, Flip-flop parameters.

UNIT-IV

Sequential Circuits :

Shift Registers – Left, Right and Bidirectional Shift Registers.

Counters - Synchronous UP/DOWN counters, Mod-N counters, Ring and Twisted Ring Counter, Frequency Divider.

Finite State Machines - Mealy and Moore models. ASM Charts, State Diagrams, Analysis and Design of Synchronous Sequential Circuits.

UNIT – V

Logic Families: Introduction, Digital IC Technology (characteristics of digital ICs), TTL - Operation, O/p configurations & characteristics, Improved TTL Series, Connecting TTL Outputs together and Tristate TTL, ECL, CMOS. IC interfacing- TTL driving CMOS and CMOS driving TTL.

Suggested Reading:

1	Zvi Kohavi & Niraj K. Jha, *Switching and Finite Automata Theory* - 3rdEdition, Cambridge, 2010.
2	R. P. Jain, 3rd edition, *Modern Digital Electronics* –Tata McGraw-Hill, 2007.
3	Morris Mano, *Digital Design*- PHI, 4th Edition,2006
4	Ronald J. Tocci, Neal S. Widmer, Gregory L. Moss, *Digital Systems - Principles and Applications*, Pearson 10th Ed, 2007.
5	Fredriac J. Hill, Gerald R. Peterson, *Introduction to Switching Theory and Logic Design*, 3rd Ed, John Wiley & Sons Inc.

PC 304 EC	PULSE AND DIGITAL CIRCUITS					
Pre-requisites	Electronic devices and circuits		**L**	**T**	**P**	**C**
			3	**-**	**-**	**3**
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To study various linear wave shaping circuits.
2	To study various non-linear wave shaping circuits.
3	To study different types of multi-vibrators.
4	To study the features of voltage and current time-base generators, data converters.
5	To study various Blocking oscillator circuits, Synchronization and frequency division circuits.

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Design various linear wave shaping circuits.
CO-2	Design various non-linear wave shaping circuits such as clippers and clamper circuits.
CO-3	Design Bi-stable, Monostable and Astable multi-vibrator circuits.
CO-4	Design sweep circuits and data converters.
CO-5	Design Blocking oscillator circuits, Synchronization and frequency division circuits.

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	3	1	1	1	-	-	-	-	1	2	1	-
CO2	3	2	2	1	1	1	-	-	-	-	1	2	2	-
CO3	2	3	3	2	2	1	-	-	-	-	1	1	1	-
CO4	3	3	3	2	1	1	-	-	-	-	1	1	-	-
CO5	2	2	1	1	1	-	-	-	-	-	1	1	-	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT–I

LINEAR WAVE SHAPING: High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square, ramp and exponential inputs. RC network as differentiator and integrator; Attenuators, its applications in CRO probe, RL circuits and their response for step input, Ringing circuit.

UNIT–II

NON-LINEAR WAVE SHAPING : Diode clippers, Transistor clippers, clipping at two independent levels, Transfer characteristics of clippers, Emitter coupled clipper; Clamping operation, clamping circuits using diode with different inputs, Clamping circuit theorem,

practical clamping circuits, effect of diode characteristics on clamping voltage, Transfer characteristics of clampers.

UNIT–III

MULTIVIBRATORS: Transistor as a switch, Switching times of a transistor, Analysis And Design of Fixed Bias, Self Bias Bistable Multi Vibrator, Commutating Capacitors, Triggering of Binary Circuits, Emitter Coupled Bistable Multivibrator (Schmitt Trigger) and its applications. Analysis and Design of Collector Coupled Monostable Multi vibrator and Astable Multivibrator, Triggering of Monostable Multivibrator, Applications of Monostable Multivibrator and Astable Multivibrator.

UNIT-IV

Sweep Circuits: General features of a time-base signal, Exponential voltage sweep circuit, basic principles of Miller and Bootstrap time-base generators, transistor Miller voltage sweep generator, transistor bootstrap voltage sweep generator, simple current sweep circuit, linearity correction through adjustment of driving waveform, transistor current time base generator. Introduction to 555 Timer and its functional diagram, sweep generator circuits using 555 timers.

Data Converters: Introduction to ADC and DAC, basic Digital to Analog conversion techniques, Weighted resistor DAC, Inverted R-2R ladder DAC, parallel comparator ADC, successive approximation ADC, Dual slope ADC, Flash type ADC, applications of ADC and DAC

UNIT – V

Blocking oscillator circuits: A triggered transistor blocking oscillator (base timing and emitter timing), An Astable transistor blocking oscillator (diode-controlled and RC-controlled), Applications of blocking oscillators.

Synchronization and frequency division: Pulse synchronization of relaxation devices, frequency division in the sweep circuits, Monostable relaxation circuits as dividers, Synchronization of sweep circuit with symmetrical signal, sine wave frequency division with a sweep circuit, a sinusoidal divider using regeneration and modulation, Synchronization of a sinusoidal oscillator with pulses.

SUGGESTED READING:

1	Jacob Millmann and Herbert Taub, *"Pulse, Digital and Switching waveforms",* 3rd Edition, Tata McGraw Hill
2	Millman J and C. C. Halkias, *Integrated Electronics*, 2nd Edition Tata McGraw
3	D. Roy Chowdary and Shail B Jain, "*Linear Integrated Circuits*", 4th Edition, New Age International (P) Limited, New Delhi, 2018.
4	Anand Kumar A, *"Pulse and Digital Circuits"*, 2nd Edition, Prentice
5	Jacob Millmann and Herbert Taub, *"Pulse, Digital and Switching waveforms",* 3rd Edition, Tata McGraw
6	David A. Bell, *"Solid State pulse circuits",* 4th Edition, Prentice Hall India.

PC 305 EC	NETWORK ANALYSIS AND SYNTHESIS					
Pre-requisites	-		**L**	**T**	**P**	**C**
			3	**0**		
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To introduce about Network Theorems and Network Topology
2	To introduce the concepts of Two Port networks, study of the different two port parameter representations and principles of two port network parameters topology description of networks
3	To introduce the concepts of resonance, complex frequency and Transient Analysis
4	To Analyze and Design different LC filters and Attenuators
5	To synthesize various networks using R, L and C combinations

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Learn how to develop and employ circuit theorems like superposition and TheveninNorton equivalent circuits etc
CO-2	Analyze given Electrical Circuit in terms of A,B,C,D and Z,Y Parameter Model and Solve the circuits and how they are used in real time applications. Able to analyze the topologic description of networks. Ability to Solve Circuits using Tree, Node, Branch, Cutset, Tie Set Methods
CO-3	Analyze small RLC circuits Series and parallel Resonance of RC, RL and RLC circuits. Able to solve Transient Analysis
CO-4	Design different types of filters and Attenuator
CO-5	Synthesize the RL, RC & RLC networks Foster and Cauer Forms

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	3	1	2	3	1	1	2	1	-	2	2	3	2
CO2	3	2	2	1	2	-	2	2	1	2	1	2	3	2
CO3	2	2	3	2	2	-	1	1	2	1	1	1	2	2
CO4	1	1	2	3	1	1	-	2	-	1	1	1	1	1
CO5	2	1	2	2	3	-	1	2	1	-	-	1	1	2

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively

UNIT–I

Network Theorems: Nodal and Mesh analysis,Superposition theorem, Thevenin‟s Theorem, Norton‟s Theorem, Maximum Power Transfer Theorem, Reciprocity Theorem and Tellegen‟s Theorem.

Network Topology: Graph, Tree, Tie set, cut set matrix, Impedance matrix formulation of node loop equations using tie-set, cut-set analysis

UNIT–II

Two port networks: Z, Y, h, g, ABCD parameters, equivalence of two ports, Condition for Symmetry and Reciprocity. T-Π transformations, inter connection of two ports networks, Brune‟s test for interconnection

UNIT–III

Response of R, L, C Networks: DC and AC excitation of RL, RC and RLC circuits, Transient Analysis. Resonance-Series and parallel. Quality factor, Bandwidth of Resonant Circuits,

UNIT-IV

Filters, Attenuators and Equalizers: Characteristic Impedance, Image Impedance, Iterative Impedance and propagation constant. Design of constant-K LP, HP, BPF, BS filters. mderived, composite filters, lattice filters. Symmetrical, Asymmetric-T, PI section networks, Introduction about Attenuators and equalizers

UNIT – V

Network Synthesis: Fosters reactance theorems, Positive real function, Hurwitz polynomial, Driving point Impedance and admittance. Synthesis of one port RC, RL and LC networks using Foster and Cauer forms.

SUGGESTED READING:

1	Van Valkenberg M.E, *Network Analysis,* 3rd edition*,* Prentice Hall of India, 1996
2	Hayt W H, Kemerly J E Durbin, *Engineering Circuit Analysis,* 7th edition, Tata McGraw Hill, 2006
3	Smarajit Ghosh, *Network Theory Analysis and Synthesis,* PHI Learning private Limited,2013
4	B. Somanathan Nair, S.R.Deepa, *Network Analysis and Synthesis,* ELSEVIER India Ltd 2012

5	S P Ghosh, A K Chakraborthy, *Network Analysis and Synthesis,* McGraw Hill Education(India)Pvt Ltd 2014

PC 351 EC	PULSE AND DIGITAL CIRCUITS LAB					
Pre-requisites	Pulse and digital circuits		**L**	**T**	**P**	**C**
			-	-	2	1
Evaluation	**SEE**	50 Marks	**CIE**		25 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To understand the response of RC
2	To study the output response of clipping and clamping circuits.
3	To understand the design concepts of multi-vibrators.
4	To study the characteristics of a Schmitt trigger and sweep circuits
5	To understand the design concepts of Analog to Digital and Digital to Analog converter.

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Design linear wave shaping circuits using RC network.
CO-2	Design clippers and clamping circuits
CO-3	Design pulse generator circuits such as multi-vibrators
CO-4	Design pulse generator circuits such as time-based generators
CO-5	Design Analog to Digital converter and Digital to Analog converter.

CO/PO	**PO 1**	**PO 2**	**PO 3**	**PO 4**	**PO 5**	**PO 6**	**PO 7**	**PO 8**	**PO 9**	**PO 10**	**PO 11**	**PO 12**	**PSO 1**	**PSO 2**
CO1	3	2	1	1	1	1	-	-	-	-	1	2	2	-
CO2	3	2	1	1	2	1	-	-	-	-	1	2	2	-
CO3	2	3	1	2	1	1	-	-	-	-	1	1	2	-
CO4	2	3	1	2	2	1	-	-	-	-	1	1	2	-
CO5	3	2	1	1	1	1	-	-	-	-	1	-	2	-

Correlation Rating: Low / Medium / High:1 / 2 /3 respectively.

Experiment - I
Study the response of RC- High pass and RC-low pass circuits for the different excitation inputs.

Experiment - II
Design of Clippers circuits

Experiment - III

Design of Clampers circuits

Experiment - IV
Design a Collector coupled Astable Multivibrator

Experiment - V
Design a Collector coupled Monostable Multivibrator

Experiment - VI
Design a Collector coupled Bitable Multivibrator

Experiment - VII
Design a Schmitt Trigger circuit using transistors

Experiment - VIII
Design a Boot strap voltage sweep circuit using transistors,

Experiment - IX
Design a Miller voltage sweep circuits using transistors

Experiment –X
Design a sweep generator circuits using 555 timers

Experiment - XI
Design a linear sweep generator circuits using 555 timers

Experiment - XII
Design a 4-bit Digital to Analog converter (DAC) using R-2R ladder network

Experiment - XIII
Design a 4-bit Digital to Analog converter (DAC) using weighted resistor network

Experiment - XIV
Design an 8-bit Analog to Digital converter (ADC) using a successive approximation method.

Experiment - XV
Design an 8-bit Analog to Digital converter (ADC) using a dual-slope integration method

General Note: Mini Project cum Design exercise:

SUGGESTED READING:

1	Robert Boylestad and Louis Nashelsky, "*Electronic Devices and Circuit theory*", 10th Edition, Prentice Hall of India Private Limited, New Delhi, 2009
2	David A. Bell, "*Laboratory Manual for Electronic Devices and Circuits*", 4th Edition, Prentice-Hall of India Private Limited, New Delhi, 2004
3	Jacob Millmann and Herbert Taub, "Pulse, Digital and Switching waveforms", 3rd Edition, Tata McGraw- Hill publishing company Limited, New Delhi, 2017

PC 352 EC	NETWORKS ANALYSIS AND SYNTHESIS LAB					
Pre-requisites	Network analysis and synthesis		**L**	**T**	**P**	**C**
			-	-	2	1
Evaluation	**SEE**	50 Marks	**CIE**		25 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To design and test different network theorems
2	To design and understand of two-port networks
3	To understand resonance phenomena of RLC resonance circuits
4	To Study the frequency response of K-LPF and K-HPF
5	To Study the frequency response of M-LPF and M-HPF

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Able to analyze and verify Different Network theorems.
CO-2	Able to understand two-port networks and resonance circuits
CO-3	Able to calculate frequency response of K-LPF and K-HPF
CO-4	Able to understand resonance phenomena of RLC resonance circuits
CO-5	Able to calculate frequency response of M-LPF and M-HPF

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	2	1	2	1	1	-	-	-	-	-	-	-	2	-
CO2	1	2	1	2	2	-	-	-	-	-	-	-	2	-
CO3	2	-	3	1	1	-	-	-	-	-	-	-	2	-
CO4	3	2	-	-	1	-	-	-	-	-	-	-	2	-
CO5	1	1	2	2	3	-	-	-	-	-	-	-	2	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

Experiment - I
Verification of Reciprocity and Tellegen‟s Theorems.

Experiment - II
Verification of Maximum Power Transfer and Superposition Theorems

Experiment - III
To calculate and verify "Z and Y" parameters of a two port network

Experiment - IV
To calculate and verify "ABCD and h-" parameters of a two port network

Experiment - V
To study the phenomenon of resonance in series RLC circuit and obtain resonant frequency

Experiment - VI
To study the phenomenon of resonance in parallel RLC circuit and obtain resonant frequency

Experiment - VII
Design and verification of Constant K Low Pass filter.

Experiment – VIII
Design and verification of Constant K High Pass filter.

Experiment – IX
Design and verification of m-Derived low pass filter.

Experiment –X
Design and verification of m-Derived high pass filter.

SUGGESTED READING:

1	Van Valkenberg M.E, *Network Analysis,* 3rd edition*,* Prentice Hall of India, 1996
2	Hayt W H, Kemerly J E Durbin, *Engineering Circuit Analysis,* 7th edition, Tata McGraw Hill, 2006
3	S P Ghosh, A K Chakraborthy, *Network Analysis and Synthesis,* McGraw Hill Education(India)Pvt Ltd 2014

SCHEME OF INSTRUCTION AND EXAMINATION
B. E. (ELECTRONICS AND COMMUNICATION ENGINEERING)

SEMESTER – IV
(With effect from AY 2025-26)

S. No	Code	Course Title	Scheme of Instruction			Contact Hrs/Wk	Scheme of Evaluation			Credits
			L	T	P		Hrs	CIE	SEE	
Theory										
1	PC401EC	Analog Electronics- II	3	-	-	3	3	40	60	3
2	PC402EC	Digital Signal Processing	3	-	-	3	3	40	60	3
3	PC403EC	Electromagnetic Theory and Transmission Lines	3	-	-	3	3	40	60	3
4	PC404EC	Probability Theory and Stochastic process	3	-	-	3	3	40	60	3
5	PC405EC	Computer Architecture and Organization	3	-	-	3	3	40	60	3
Practicals										
6	PC451EC	Analog Electronics Lab	-	-	3	3	3	25	50	1.5
7	PC452EC	Digital Signal Processing Lab	-	-	3	3	3	25	50	1.5
Total			**15**	**-**	**6**	**21**	**21**	**250**	**400**	**18**

PC 401 EC	ANALOG ELECTRONICS II					
Pre-requisites	Analog Electronics-I		**L**	**T**	**P**	**C**
			3			**3**
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	Analyze frequency response of Amplifiers in different frequency ranges.
2	Familiarize with concept and effect of negative feedback
3	Study positive feedback and Design different types of oscillators.
4	Design Power Amplifiers and calculate their efficiencies.
5	Familiarize with concept of tuned Amplifiers

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Identify the type of negative feedback, Analyze and design of negative feedback amplifiers.
CO-2	Design Audio Frequency and Radio Frequency oscillators
CO-3	Distinguish between the classes of Power Amplifiers and their design considerations.
CO-4	Compare the performance of single and double Tuned Amplifiers.
CO-5	Able to demonstrate an understanding of operational amplifiers and their applications

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	2	-	1	-	-	1	1	-	-	-	2	-
CO2	3	2	1	2	1	-	-	1	-	-	-	-	1	-
CO3	3	2	2	-	1	-	-	1	-	--	-	-	-	-
CO4	2	1	2	-	-	-	-	1	1	-	-	-	2	-
CO5	3	2	2	2	1	-	-	1	2	-	-	-	-	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT–I

Feedback Amplifiers Analysis and Design: The feedback concept, General characteristics of negative feedback amplifier, Effect of negative feedback on input and output impedances, Voltage and current, series and shunt feedbacks. Stability considerations .

UNIT–II

Oscillators Analysis and Design: Positive feedback and conditions for sinusoidal oscillations, RC oscillators, LC oscillators, Crystal oscillator, Amplitude and frequency stability of oscillator.

UNIT–III

Large Signal Amplifiers: BJT as large signal audio amplifiers, Classes of operation, Harmonic distortion, power dissipation, efficiency calculations. Design considerations of transformer coupled and transform less push-pull audio power amplifiers under Class-A. Class-B, Class c and Class-AB operations

UNIT-IV

RF Voltage Amplifiers: General consideration, Analysis and design of single tuned and double tuned amplifiers with BJT, Selectivity, gain and bandwidth. Comparison of multistage, single tuned amplifiers and double tuned amplifiers. The problem of stability in RF amplifiers, introduction to staggered tuned amplifiers.

UNIT – V

Operational Amplifier Applications: Inverting and non-inverting amplifiers with ideal and non-ideal op-amps, voltage followers, Difference Amplifier, Summing amplifiers, ideal and practical Integrator, Differentiator, Voltage to current and current to voltage converters, precision Rectifiers

SUGGESTED READING:

1	Jacob Millman, Christos C. Halkias, and Satyabrata Jit, Electronic Devices and Circuits, 3rd ed., McGraw Hill Education, 2010.
2	David A. Bell, Electronic Devices and Circuits, 5th ed., Oxford University Press, 2009
3	S Salivahanan, N Kumar, and A Vallavaraj, Electronic Devices and Circuits, 2nd ed., McGraw Hill Education, 2007.
4	Jacob Millman, Christos Halkias, Chetan Parikh, Integrated Electronics, 2nd ed., McGraw Hill Education (India) Private Limited, 2011.
5	Ramakanth A. Gayakwad, "*Op-amps and Linear Integrated Circuits*", 3rd Edition, Prentice-Hall of India private Limited, New Delhi, 1995.

PC 402 EC	DIGITAL SIGNAL PROCESSING					
Pre-requisites	-		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To study about discrete time systems and to learn about the DFT and FFT algorithms.
2	To study the design techniques for FIR and IIR digital filters
3	To study the finite word length effects in signal processing
4	To understand Multi rate signal processing
5	To study the architecture of TMS processor

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Find DFT of a given signal through Fast Fourier Transform techniques.
CO-2	Design FIR and IIR type digital filters.
CO-3	Identify filter structures and evaluate the coefficient quantization effects.
CO-4	Understand sample rate conversion techniques.
CO-5	Compare the architectures of DSP and General Purpose Processors.

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	1	3	2	-	-	2	1	3	-	2	3	-
CO2	1	2	3	2	3	-	-	1	2	2	-	2	3	-
CO3	3	2	1	2	3	-	-	3	3	2	-	3	3	-
CO4	3	1	2	2	2	-	-	2	2	3	-	2	2	-
CO5	1	3	2	3	2	-	-	3	2	2	-	2	2	2

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT– I

Introduction: Concept of frequency in continuous and discrete time signals, DFT and its properties, linear convolution, circular convolution. Computational complexity of direct Computation of DFT, Fast Fourier Transform, DIT and DIF, FFT algorithms for RADIX-2 case, in-place computation, Bit reversal, Finite word length effects in FFT algorithms, Use of FFT in Linear Filtering

UNIT - II

FIR Filters: FIR digital filter design techniques. Properties of FIR digital filters, design of FIR filters using windows and frequency sampling techniques, linear phase characteristics. Realization diagrams for IIR and FIR filters, finite word length effects

UNIT– III

IIR Filters: Analog filter design – Butterworth and Chebyshev approximations, IIR digital filter design techniques, impulse invariant technique. Bilinear transform technique. Comparison of FIR and IIR filters, frequency transformations.

UNIT– IV

Multirate signal processing: Introduction, decimation by a factor D, interpolation by a factor I, sampling rate conversion by a rational factor I/D, design of practical sampling rate converter, S/W implementation of sampling rate converter, application of Multirate signal processing.

UNIT–V

DSP Processors: Introduction to Fixed point Digital Signal Processors, TMS 320C54XX processor-architecture, addressing modes, instruction set, Assembly programming, programming issues, Applications of DSP processors.

Suggested Reading:

1	John G.Proakis and Dimitris G. Manolakis, *"Digital Signal Processing principles, Algorithms and Applications"*, 3rd Edition, Prentice-Hall of India Private Limited, New Delhi, 1997.
2	Alan V. Oppenheim and Ronald W. Schafer, "*Discrete Time Signal Processing*", 3rd edition, Prentice Hall, Upper Saddle River, NJ,2010.
3	Sanjit K. Mitra, "*Digital Signal Processing: A Computer-Based Approach*", 4/e, McGraw-Hill, New York,2011.
4	Avatar sing and S.Srinivasan, *"Digital Signal Processing implementation using DSP Microprocessors with Examples from TMS320C54XX"*, Thomson Books Icole, 2004.

PC 403 EC	ELECTROMAGNETIC WAVES AND TRANSMISSION LINES					
Pre-requisites	-		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To become familiar with the fundamental concepts of electrostatics and magneto static laws and their applications.
2	To familiar with the four Maxwell‟s equations used to study time varying EM or dynamic fields and apply them to solve practical EM problems.
3	To acquaint with theoretical analysis of the characteristics of electromagnetic waves in a wide variety of Practical Mediums.
4	To familiar with fundamentals of Transmission line theory.
5	To acquaint with transmission line impedance calculations

Course Outcomes :	
Students will be:	
CO-1	Able to express and elaborate Maxwell's Equations in differential and integral forms and the constitutive relations between the flux densities and field intensities of the electrostatics, magneto-statics.
CO-2	Able to express the time varying fields in integral and differential form of Maxwell's Equations.
CO-3	Able to derive the Helmholtz wave equations in its various forms and the wave nature of their solutions for time-harmonic waves in various mediums.
CO-4	Able to apply fundamental electromagnetic concepts in applications such as Transmission Lines and Antennas.
CO-5	Able to understand the impedance matching concepts using the smith chart

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	-	-	-	-	-	-	-	-	-	-	3	2
CO2	3	3	1	-	-	-	-	-	-	--	-	-	3	2
CO3	2	3	-	-	-	-	-	-	-	-	-	-	2	2
CO4	3	-	-	1	1	1	1	-	-	-	-	-	2	2
CO5	3	2	2	2	-	-	-	-	-	-	-	-	3	3

Correlation rating : Low / Medium / High : 1 / 2 / 3 respectively.

UNIT - I

Electrostatics: Review of Vector Calculus and Coordinate systems and Transformation, Coulomb‟s Law, Electric Field Intensity, Electric field due to different charge distributions Electric Field due to Line Charge, Sheet Charge and Volume Charge Distribution. Electric Flux, Flux Density, Gauss‟s Law and Applications. Energy and Potential, Potential Field of a Point Charge, System of Charges, potential gradient, Energy density in Electrostatic fields, Electric Dipole, convection and conduction currents, continuity equation and relaxation time, Poisson‟s and Laplace‟s Equations, Capacitance and Capacitors.

UNIT - II

Magneto-statics: Biot-Savart Law, Ampere‟s Circuital Law, Applications of Ampere‟s Law, Magnetic Flux Density, Magnetic Scalar and Vector Potentials, Forces due to magnetic fields, Magnetic Dipole, Magnetization, Inductors and Inductances, Magnetic Energy.

UNIT - III

Time Varying Fields and Maxwell's Equations: Faraday‟s Law, Transformer and Motional EMF‟s, Displace Current, Maxwell‟s Equations in Differential and Integral Forms, TimeVarying Potentials, Electromagnetic Boundary Conditions, Time-Harmonic Fields.

UNIT - IV

EM Wave Propagation: Uniform Plane Wave, Wave Propagation in Free Space, Dielectrics, Good Conductors-Skin Effect. Poynting‟s Theorem and Wave Power, Poynting Vector, Instantaneous, average and complex pointing vector, Wave Polarization-Linear, Circular and Elliptical polarizations, Reflection of Uniform Plane Waves at Normal incidence and Oblique incidence angles, Reflection coefficient, Transmission coefficient, power and energy calculations.

UNIT - V
Transmission Lines: Circuit representation, Equations of voltage and current on transmission line, propagation constant and characteristic impedance, Loss less Line, Distortion less Line, Infinite line concepts, Input impedance relations of open and short-circuited transmission lines, reflection coefficient and VSWR. The Smith Chart, Transmission Line Impedance MatchingImpedance Matching by Quarter wave Transformer, Single Stub Matching and Double Stub Matching..

Suggested Reading:

1	Matthew N, O. Sadiku, *Principles of Electromagnetics*, Oxford University Press, 2009, 4th edition.
2	David K.Cheng, *Field and Wave Electromagnetics*, Pearson Education, 2001, 2nd edition
3	W.H.Hayt, Jr.and J.A Buck,*Engineering Electromagnetics*, Tata Mc Graw-Hill, 2006, 7thedition

PC 404 EC	PROBABILITY THEORY AND STOCHASTIC PROCESSES					
Pre-requisites	Probability Theory Basics		**L**	**T**	**P**	**C**
			-	**3**	**-**	**3**
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objective of enabling the student to:	
1	Understand different types of random variables & their density distribution functions.
2	Learn one random variable characteristic function of different variables using their density functions.
3	Learn the concepts of sequences of random variables, Properties of Random vectors.
4	Understand elementary concepts of the Rom Processes or distribution functions.
5	Understand the functions of two random variables‟ probability density distribution of the joint random variables.

Course Outcomes:	
On completion of this course, the student will be able to:	
CO-1	Solve using an appropriate sample space by the concepts of probabilities and
CO-2	Understand multiple random variables relate the same through examples to real problems.
CO-3	Characterize LTI systems‟ response driven by a stationary random process using autocorrelation and power spectral density functions.
CO-4	Apply these principles in areas where noise is a serious challenge.
CO-5	Understand the usefulness of stochastic processes in their professional area.

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	3	-	-	-	1	-	-	-	-	-	-	1	-
CO2	3	2	-	-	-	-	-	-	-	-	-	-	1	2
CO3	3	2	-	1	2	-	-	-	-	-	-	-	-	-
CO4	3	2	-	1	-	-	-	-	-	-	-	-	2	-
CO5	3	2	-	1	2	-	-	-	-	-	-	-	-	-

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT–I

Concepts of Probability and Random Variable: Definitions, Probability Induction, Causality versus Randomness, Review of Set Theory, Probability Space, Conditional Probability. Repeated Trials Combined Experiments, Bernoulli Trials, Bernoulli‟s Theorem Games of Chance. Random Variable: Definition, Distribution Density Functions, Specific Random Variables their probability density distribution functions: Normal, Exponential, Gamma, Chi-Square, Raleigh, Nakagami- m, Uniform, Beta, Cauchy, Laplace Maxwell, Bernoulli, Binomial, Poisson,

Geometric, Negative Binomial Conditional Distributions, Asymptotic Approximations for Binomial Rom Variable.

UNIT–II

Functions of One Random Variable: Function of a Random Variable $g(\mathbf{x})$, The Distribution of $g(\mathbf{x})$, Mean, Variance, and Moments. Characteristic Functions of random variables with the above distributions.

UNIT–III

Two Random Variables: Bi-variate Distributions, One Function of Two Random Variables, Two Functions of Two Rom Variables, Joint Moments, Joint Characteristic Functions, Conditional Distributions, and Conditional Expected Values

UNIT-IV

Sequences of Random Variables: General Concepts, Conditional Densities, Characteristic Functions, Normality, Mean Square Estimation, Stochastic Convergence Limit Theorems. Random Numbers: Meaning, Generation of random sequence, pseudo-random binary sequence. Applications of random numbers.

UNIT – V

Stochastic Processes: General elementary concepts definitions of stationary, Ergodic, random processes independence, spectral density, white color noise, Response to linear systems stochastic inputs, Markov Processes.

SUGGESTED READING:

1	A Papoulis, S.U. Pillai, “Probability, Rom Variables Stochastic Processes”, 4th edition, Tata McGraw-Hill, 2008.
2	Peyton Z Peebles, “Probability, Rom Variables & Rom Signal Properties”, 4th edition, Tata McGraw-Hill, 2001.
3	Carl Helstrom, “Probability Stochastic Processes for Engineers”, Macmillan Publishing Company, 1984.
4	Richard H. Williams, “Probability, Statistics, Rom Processes for Engineers”, Thomson Learning, 1st edition, 2003.

PC 405 EC	COMPUTER ARCHITECTURE AND ORGANIZATION					
Pre-requisites	-		**L**	**T**	**P**	**C**
			3	-	-	3
Evaluation	**SEE**	60 Marks	**CIE**		40 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To familiarize with Central Processing Unit (CPU) concepts.
2	To understand register, architecture, addressing modes and instruction set of Intel
3	microprocessor To design data path and control units of Central Processing Unit (CPU)
4	To know IO processor and cache memory organization.
5	To understand CPU performance enhancement strategies

Course Outcomes :	
On completion of this course, the student will be able to :	
CO1	Design Arithmetic and Logic Unit for the given specifications.
CO2	Demonstrate data path and control unit realizations of CPU.
CO3	Analyze cache memory and IO organizations
CO4	Incorporate pipeline concept in a Central Processing Unit (CPU).
CO5	Develop programs of Intel Microprocessor

	PO 1	**PO 2**	**PO 3**	**PO 4**	**PO 5**	**PO 6**	**PO 7**	**PO 8**	**PO 9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PS02**
CO 1	3	1	-	3	-	-	-	-	3	-	2	2	2	3
CO 2	3	3	3	2	3	-	-	-	3	2	3	3	2	3

CO 3	2	-	1	-	-	-	-	-	-	-	-	1	2	3
	PO 1	**PO 2**	**PO 3**	**PO 4**	**PO 5**	**PO 6**	**PO 7**	**PO 8**	**PO 9**	**PO1 0**	**PO1 1**	**PO1 2**	**PSO 1**	**PS0 2**
CO 4	3	3	3	2	2	-	-	-	2	2	2	2	2	3
CO 5	3	3	3	3	3	-	-	-	3	3	3	3	3	3

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

UNIT– I

CPU Organization: Register Transfer Language (RTL), Common bus structure, Arithmetic, Logic and Shift Unit using multiplexer, Design of Basic CPU, HDL Realization of Basic CPU

UNIT– II

Data Path Design: Fixed-Point Arithmetic: Addition, Subtraction, Booth‟s algorithms for multiplication, Array Multiplier and Wallace tree multiplication, Division - Restoring and Nonrestoring algorithms, Overhead in floating point arithmetic, HDL descriptions of Fixed-Point arithmetic

UNIT– III

Control Design: Basic concepts, Hardwired Control unit design approach: classical and one-hot methods, Micro-programmed Control unit approach: basic concept, micro-program sequencer, Design examples: control unit designs for GCD processor, DMA controller and CPU control unit.

UNIT– IV

Memory Organization: Memory Organization: Memory hierarchy, Main memory: RAM, ROM, DRAM, Multi

System Organization: System Organization: communication methods, IO and system control: Programmed IO, DMA and interrupts and Input

UNIT–V

Performance Enhancement Strategies: Reduced Instruction Set Computer (RISC): characteristics and architecture, Parallel processing: Pipeline – Arithmetic and Instruction, Pipeline Conflicts,
The 8086 Microprocessor Family- Overview, 8086 architectures, Flag Register, Segmented memory, Maximum and Minimum mode of operation, Addressing modes, Memory read and Write bus cycles, Instruction Set: data transfer, arithmetic, logical, program Jumps and String instructions, Addressing Modes and Practicing Programs of 8086

Suggested Reading:

1	Morris Mano M, *Computer System Architecture*, 3rd edition, Prentice Hall India, 2007.
2	John P. Hayes, *Computer Architecture and Organization*, 3rd edition, McGraw Hill, 1998.
3	Douglas V.Hall, "Microprocessors and Interfacing Programming and Hardware", 2nd Edition, Tata McGraw- Hill publishing company Limited, New Delhi, 1994.

PC 451 EC	ANALOG ELECTRONICS LAB					
Pre-requisites	Analog Electronics-I and Analog Electronics -II		**L**	**T**	**P**	**C**
			-	-	2	1
Evaluation	**SEE**	50 Marks	**CIE**		25 Marks	

Course Objectives:	
The course is taught with the objectives of enabling the student to:	
1	To understand the design concepts of transistor amplifiers at high frequency.
2	To understand the design concepts of multi stage amplifiers.
3	To understand the design concepts of feedback amplifiers.
4	To understand the design concepts of oscillators.
5	To study the design concepts of power and tuned amplifiers.

Course Outcomes:	
On completion of this course, the student will be able to :	
CO-1	Design the different types of feedback amplifiers.
CO-2	Implement RC & LC oscillator circuits for the given specifications.
CO-3	Design and analyze various tuned amplifiers
CO-4	Determine the frequency response of tuned amplifiers
CO-5	Design simple circuits using Opamps

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	2	-	1	-	-	1	1	-	-	-	2	-
CO2	3	2	1	2	1	-	-	1	-	-	-	-	1	-
CO3	3	2	2	-	1	-	-	1	-	-	-	-	2	-
CO4	2	1	2	-	-	-	-	1	1	-	-	-	1	-
CO5	3	2	2	2	1	-	-	1	2	-	-	-	3	-

Correlation Rating: Low / Medium / High:1 / 2 /3 respectively.

Experiment - I
Design and Analysis of frequency response of multistage stage RC coupled amplifier using BJT and FET

Experiment - II
Design and Analysis of frequency response of transformer coupled amplifier

Experiment - III
Verification of Miller‟s Theorem

Experiment - IV
Design and analysis of Darlington Bootstrap amplifier
Experiment - V
Design and Analysis of Voltage Series Voltage Shunt Feedback Amplifier

Experiment - VI
Design and Analysis of Current Series Current Shunt Feedback Amplifier

Experiment - VII
Design and Analysis of RC Phase Shift Oscillator, Wien Bridge Oscillator

Experiment - VIII
Design and Analysis of Colpitts and Hartley Oscillators,

Experiment - IX
Design and verification of Inverting and non-inverting amplifiers and summing amplifier using operational amplifier.

Experiment –X
Design, testing of IF and RF tuned Amplifier with load.

Experiment - XI
Design the Class „A‟ power Amplifier and verify characteristics of power amplifier.

Experiment - XII
Design the Class „B‟ power Amplifier and verify characteristics of power amplifier.

SUGGESTED READING:

1	Robert Boylestad and Louis Nashelsky, "*Electronic Devices and Circuit theory*", 10th Edition, Prentice Hall of India Private Limited, New Delhi, 2009
2	David A. Bell, "*Laboratory Manual for Electronic Devices and Circuits*", 4th Edition, Prentice-Hall of India Private Limited, New Delhi, 2004

PC 452 EC	DIGITAL SIGNAL PROCESSING LAB					
Pre-requisites			**L**	**T**	**P**	**C**
			-	-	3	1.5
Evaluation	**SEE**	50 Marks	**CIE**		25 Marks	

Course Objectives :	
The course is taught with the objectives of enabling the student:	
1	To understand the concept of basic signals and to generate them using MATLAB.
2	To understand the concept of N-point FFT algorithm.
3	To understand the concept of analog and digital filters and simulation using MATLAB.
4	To study the architecture of TMS320 C54x.
5	To understand the concept of Linear Convolution and simulate it using CCSTUDIO

Course Outcomes :	
On completion of this course, the student will be able to :	
CO-1	Examine the frequency response and impulse response of discrete-time LTI systems
CO-2	Interpret discrete-time signals using DFT
CO-3	Apply FFT algorithms for various signal processing operations.
CO-4	Analyze IIR and FIR digital filters
CO-5	Design IIR and FIR digital filters for real time DSP applications

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	2	3	3	-	-	3	2	2	-	2	3	-
CO2	3	2	3	2	2	-	-	2	2	3	-	2	3	-
CO3	-	2	2	2	2	-	-	-	2	-	-	-	3	-
CO4	3	2	3	2	2	-	-	2	2	3	-	2	3	-
CO5	2	3	2	3	3	-	-	3	2	2	-	2	3	-

Experiment - I
(a)Generation of basic signals based on recursive difference equations. (b) Operations on Basic sequences

Experiment - II
(a) Linear and Circular Convolutions in time domain and frequency domain (b) Determination of autocorrelation and Power Spectrum of a given signal(s)

Experiment - III
(a) Fast Fourier Transform – DIT and DIF algorithm (b) Spectrum analysis using DFT

Experiment - IV
(a) Generation of windows – Rectangular, Hamming and Hanning window (b) Design of LPF, HPF, BPF and BSF using windowing technique

Experiment - V
(a) Design of Butterworth Filter using Impulse Invariant and Bilinear transformation (b) Design of Chebyshev Filter using Impulse Invariant and Bilinear transformation

Experiment - VI
(a) Implementation of Decimation and Interpolation Process. (b) Implementation of I/D sampling rate converters

Experiment - VII
(a) Study of TMS320C54X DSP processor (b) Arithmetic operation using TMS320C54XX

Experiment - VIII
MAC operation using various addressing modes

Experiment - IX
(a)Perform Linear Convolution of given sequences/Signals (b)Perform Circular Convolution of given sequences/Signals

Experiment –X
(a) FFT Implementation (b) Waveform Generation – Sine wave and Square wave

Experiment - XI
Implementation of FIR filter on DSP processor

Experiment - XII
Implementation of IIR filter on DSP processor .

SUGGESTED READING:

1	John G Proakis, Vinay K. Ingle, “Digital Signal Processing Using MATLAB”, Third Edition, © 2012 by Cengage Learning
2	Sanjit K. Mitra, “Digital Signal Processing: A Computer - Based Approach” second edition,McGrawHill
3	B. Preetham Kumar, “Digital Signal Processing Laboratory”, © 2005 by CRC Press